

Abstract of the Disclosure:

The invention relates to a method and to a device for adapting/tuning signal transit times on line systems or networks between integrated circuits which are mounted on printed circuit boards. Fine tuning of differences in transit times can be easily carried out by capacitive load structures disposed on conductor tracks (3-9) in the vicinity of chip housings of the integrated circuits on the printed circuit boards by disconnecting connecting lines (20, 21) using a cutting laser (Fig. 1B).

15 MPW/kc